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01-9-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al.
Serial No.: 09/941,952
Filed : August 29, 2001
Assignee : Intel Corporation
Title : SIMULATING A LOGIC DESIGN

Art Unit: 2123
Examiner:

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Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Each enclosed document listed on the herewith Form PTO-1449 was cited in the attached International Search Report dated December 5, 2003, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any

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Applicant : William R. Wheeler et al.
Serial No.: 09/941,952
Filed : August 29, 2001
Assignee : Intel Corporation

Attorney's Docket No.:10559-597001

communication from a foreign patent office in a counterpart
foreign application.

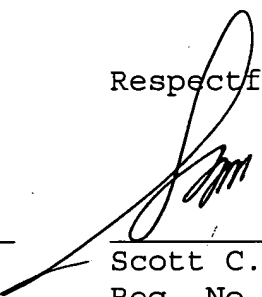
The documents are in the English language; hence no concise
explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the
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Examiner's initials in the left column per MPEP 609 are
earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit
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Respectfully submitted,

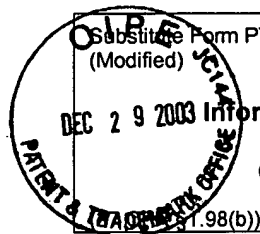
Date: 10/02/03



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 Substitute Form PTO-1449
(Modified)

 U.S. Department of Commerce
Patent and Trademark Office

 Attorney's Docket No.
10559-597001

 Application No.
09/941,952

**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

 Applicant
William R. Wheeler et al.

 Filing Date
August 29, 2001

 Group Art Unit
2123

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,220,512	06/1993	Watkins, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	0 433 066	06/1991	EPO				
	AM	0 901 088	03/1999	EPO				
	AN	1 065 611	01/2001	EPO				
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
	AR	
	AS	
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.